

Software Release Notes, MK.4 ESD & Latch-Up Test System

Release 7.3 (Software, Middleware, and Firmware)

New Release: 7.3
Date: 2007-06-28
Previous Release: 7.2

Component	Version	Previous Version	Part Number
Software	1.14.4	1.14.3	70-901-205-00
Middleware	1.49.0.3	1.49.0.0	70-901-305-00
Firmware	1.00.20070215	1.00.20070215	70-901-305-00

Effectivity

This is a general release, applicable to all production and field systems.

Dependencies

The versions of the three components in this release (PC Software, Tester Middleware, and Tester Firmware) have been developed and tested to work with each other. Use of other version combinations is not recommended.

New features

PC Software

1. Added import capability for legacy mapping files (*.zpm, *.dsm).
2. When importing vectors, if the vector file is not found (Mk2/Paragon save vector references as absolute path to the vector file), the user is prompted about the new location
3. Vector editor will now support vector files that can have up to 256 KB (262144 bytes) worth of vector states per pin

Fixes

PC Software

1. When importing legacy vector files, the assignment of groups in the first vector voltage level object is done incorrectly – all device groups are assigned instead of just vectored groups
2. When applying the configuration change to a device document, an error is reported during the save operation

3. Vector editor is not working correctly if a vector group is added thru the changing of the group's type

Middleware

1. When running vectors the script crashes while applying the specified delay

Known issues

1. Uploading large vector data sets can take considerable amount of time. The upcoming release will address this problem.
2. Preconditioning vectors are incorrect for the 1.2KHz vector clock speed. This frequency causes the stress pulse to disappear and the parking level to be ignored. It occurs only with this frequency and it occurs regardless of whether or not the stress pin is actually being vectored. The problem will be fixed in the next release. For now, avoid using the 1.2KHz vector clock frequency.

Additional documentation

Figures